

Revised 24/3/80
Revised 28/5/80

ROM : PRM 8 Circuitboard (refer to diagram PRM-8/C3(GE))

Link "IC6" pins 1 and 8
Link "IC4" pins 1 and 8

Fit the Firmware as follows:

TB VI.2 (1) ic 15
TB VI.2 (2) ic 14

RAM :

MXA-3 Circuit Board

Link "L1" to "0" (ic 4 pin 4)
Link "PS1" to "1" (ic 4 pin 11)If the board is partially loaded with 2114's
start with ic 14 and ic 15. and work towards
ic 7, and ic 8.If 8k RAM is used for TB VI.2 connect the following
link - "PS2" to "2" (ic 4 pin 10).
Start loading the second 4k block at ic 22 and ic 23

VDU G.

Cut Address link from VS to "4"
insert link from VS to "0"(connect PS1 (edge connector pin 21) to PAGE C
(See DCR-6)

DCR-6

* DO NOT FIT IC4 *

Use appendix 1 of 'TB VI.2 User manual' to make
page select signals for PAGE C and PAGE DConnect PAGE C to PS1 on the VDU G board (we use
edge connector pin 21)

Connect PAGE D to PS (IC 2 pin 1)

A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0
1	1	0	1	0	1	1	1	1	0	Φ	Φ	Φ	Φ	Φ	Φ
PAGE D						S1	S2	S3	S4	S5	S6	DON'T CARE			

Connect the following:

S1 0V
S2 5V
S3 5V
S4 5V
S5 5V
S6 0V

Keyboard Address D780

Take care use link labels S1-S6 as shown on
the layout sheet supplied with the board.If using 81LS96 (inverting buffer) link IC1 pin 9
to IC 5 pin 2If using 81LS95 (non inverting buffer) link IC1 pin 10
to IC 5 pin 2